NIE 20 MM SOLVER

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of

Naoki FURUHATA

Serial No. 09/848,263

Filed May 4, 2001

GROUP 2811

Examiner

MONOLITHIC COMPOUND SEMICONDUCTOR INTEGRATED CIRCUIT AND

METHOD OF FORMING THE SAME

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were discovered during any searches they or their client had made, or that they were considered in the preparation of the application.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that no item contained in this

Information Disclosure Statement was known to any individual

IC S800 WAIL ROOM

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2 8.27-01 RFb/c

Serial No. 09/848,263

designated in \$1.56(c), more than three months prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

Βv

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August 20, 2001